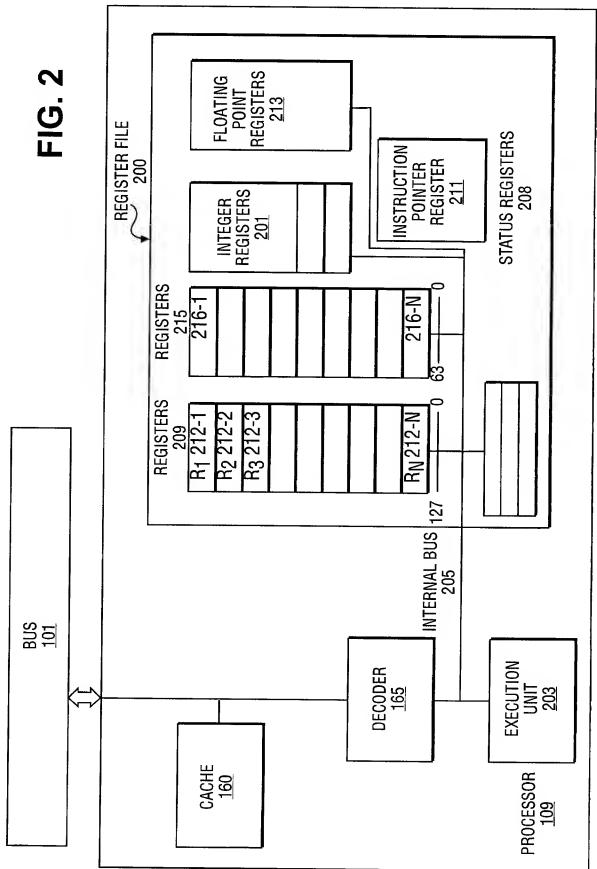
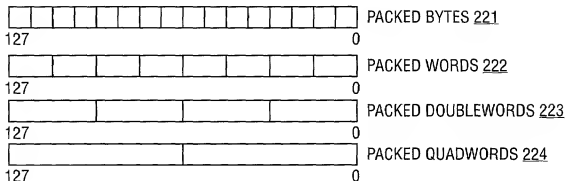


**FIG. 1**

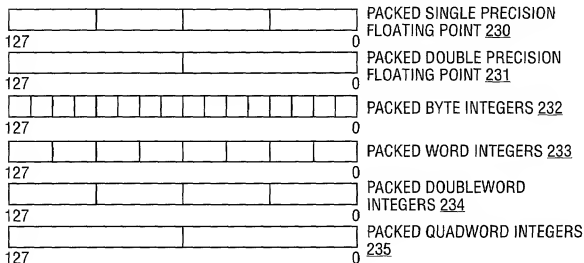


# FUNDAMENTAL 128-BIT PACKED SIMD DATA TYPES



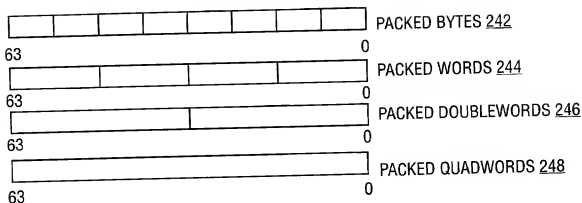
**FIG. 3A**

# 128-BIT PACKED FLOATING-POINT AND INTEGER DATA TYPES



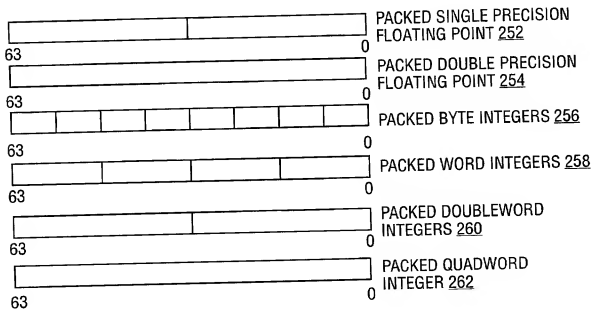
**FIG. 3B**

# 64-BIT PACKED SIMD DATA TYPES 240



**FIG. 3C**

# 64-BIT PACKED FLOATING POINT & INTEGER DATA TYPES 250



**FIG. 3D**

$$S = \begin{bmatrix} S_{11} & S_{12} & S_{13} & S_{14} \\ S_{21} & S_{22} & S_{23} & S_{24} \\ S_{31} & S_{32} & S_{33} & S_{34} \\ S_{41} & S_{42} & S_{43} & S_{44} \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix}$$

SOURCE MATRIX 300

**FIG. 4A** MATRIX SUBDIVISION

$$A = \begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix} = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} \quad B = \begin{bmatrix} S_{13} & S_{14} \\ S_{23} & S_{24} \end{bmatrix} = \begin{bmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{bmatrix}$$

SUB-MATRIX A - 310

SUB-MATRIX C - 320

$$C = \begin{bmatrix} S_{31} & S_{32} \\ S_{41} & S_{42} \end{bmatrix} = \begin{bmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{bmatrix} \quad D = \begin{bmatrix} S_{33} & S_{34} \\ S_{43} & S_{44} \end{bmatrix} = \begin{bmatrix} D_{11} & D_{12} \\ D_{21} & D_{22} \end{bmatrix}$$

SUB-MATRIX C - 330

SUB-MATRIX D - 340

**FIG. 4B**

$$\begin{aligned} 312 \sim A.V_1 &= \begin{bmatrix} A_{11}, A_{12} \\ A_{21}, A_{22} \end{bmatrix} & 322 \sim B.V_1 &= \begin{bmatrix} B_{11}, B_{12} \\ B_{21}, B_{22} \end{bmatrix} & 332 \sim C.V_1 &= \begin{bmatrix} C_{11}, C_{12} \\ C_{21}, C_{22} \end{bmatrix} & 342 \sim D.V_1 &= \begin{bmatrix} D_{11}, D_{12} \\ D_{21}, D_{22} \end{bmatrix} \\ 314 \sim A.V_2 &= \begin{bmatrix} A_{11}, A_{12} \\ A_{21}, A_{22} \end{bmatrix} & 324 \sim B.V_2 &= \begin{bmatrix} B_{11}, B_{12} \\ B_{21}, B_{22} \end{bmatrix} & 334 \sim C.V_2 &= \begin{bmatrix} C_{11}, C_{12} \\ C_{21}, C_{22} \end{bmatrix} & 344 \sim D.V_2 &= \begin{bmatrix} D_{11}, D_{12} \\ D_{21}, D_{22} \end{bmatrix} \end{aligned}$$

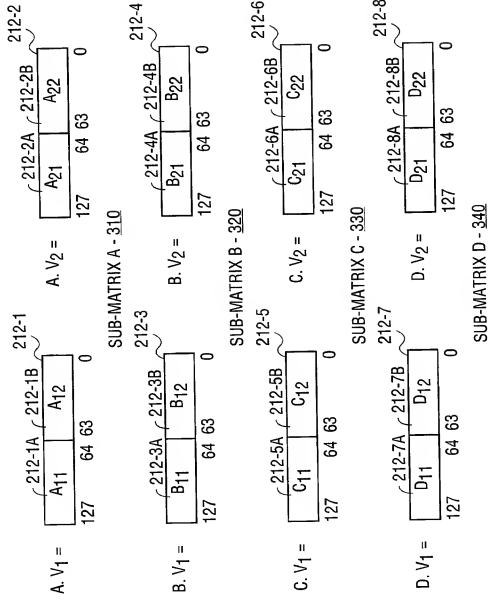
SUB-MATRIX A ROWS

SUB-MATRIX B ROWS

SUB-MATRIX C ROWS

SUB-MATRIX D ROWS

**FIG. 4C** SUB-MATRIX ROW REPRESENTATION



REGISTER REPRESENTATION

FIG. 4D

404  
dX <= |X| DETERMINANT CALCULATION 400

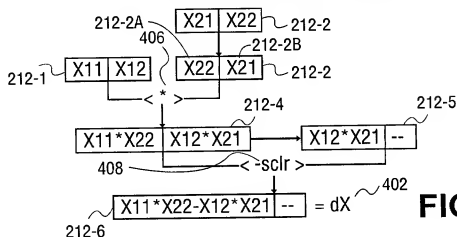


FIG. 5

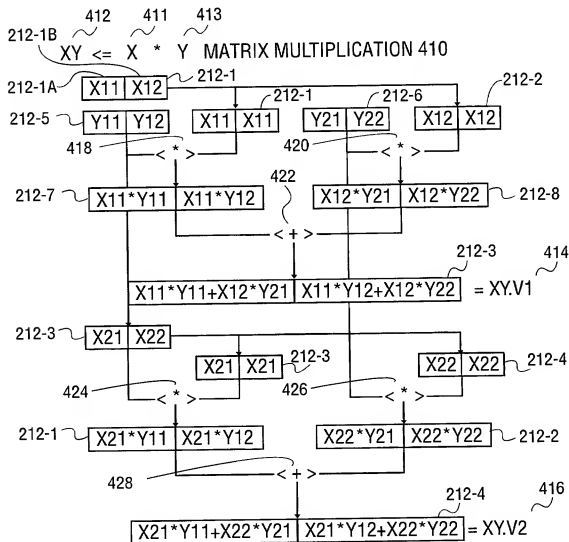


FIG. 6

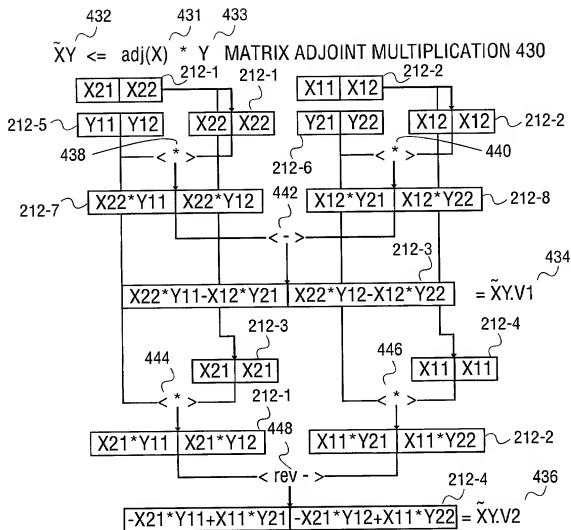


FIG. 7



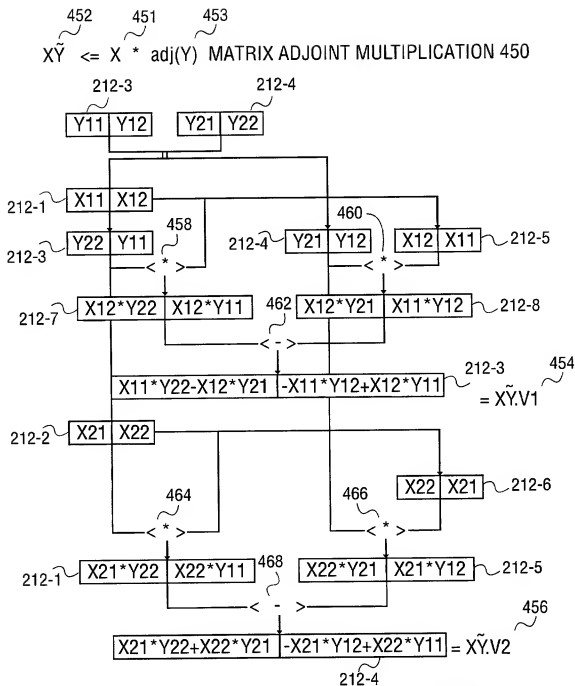
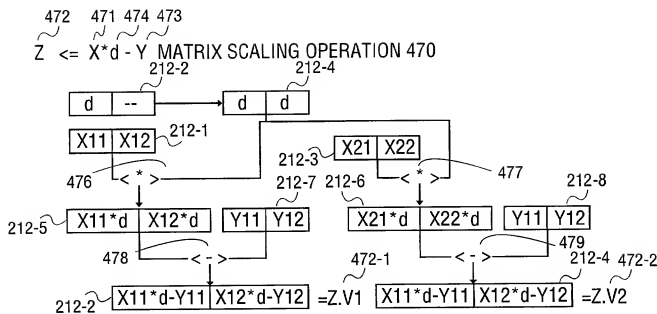
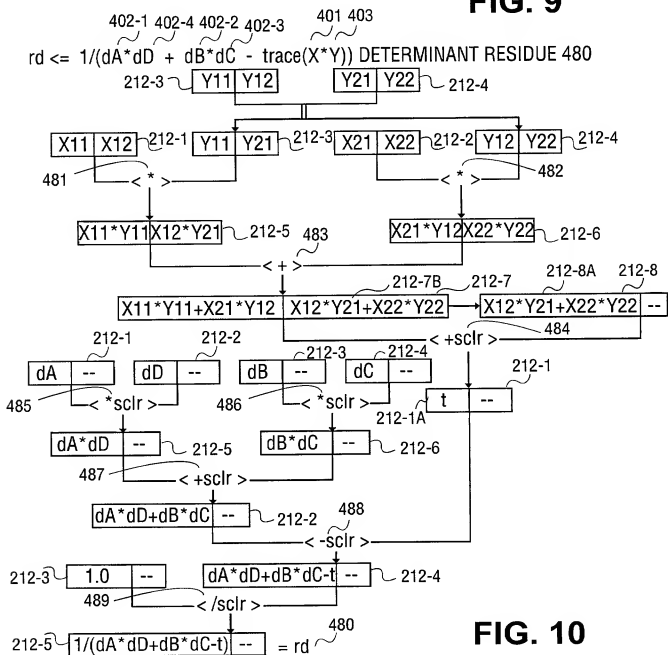


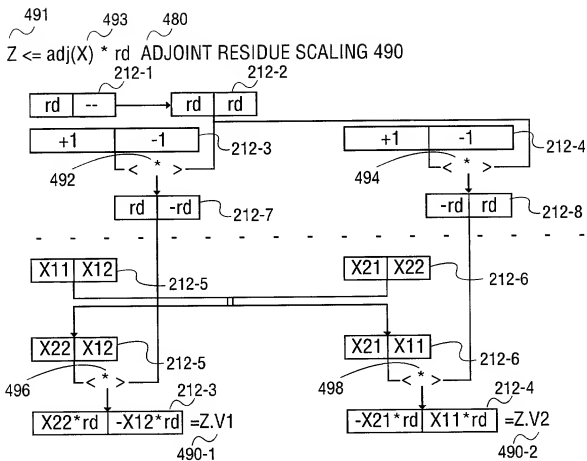
FIG. 8



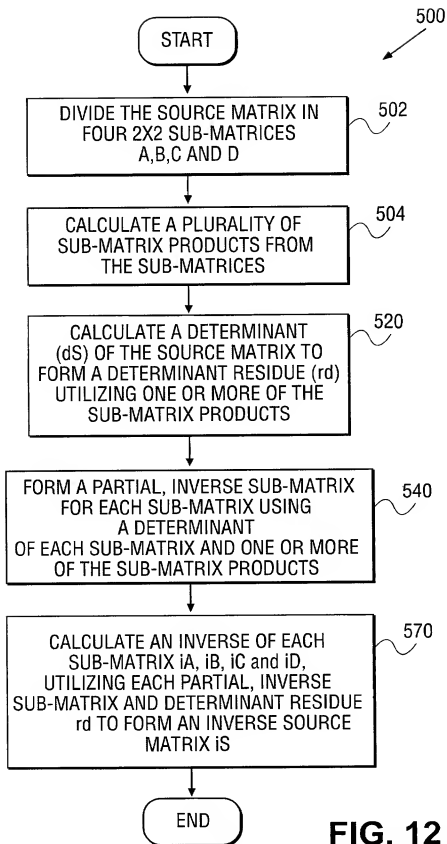
**FIG. 9**



**FIG. 10**



**FIG. 11**



**FIG. 12**

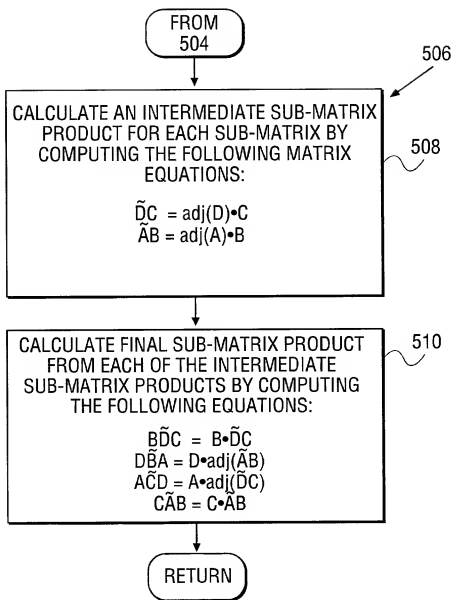
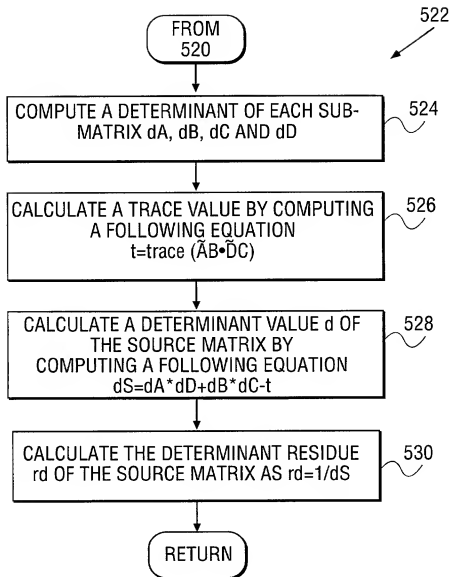
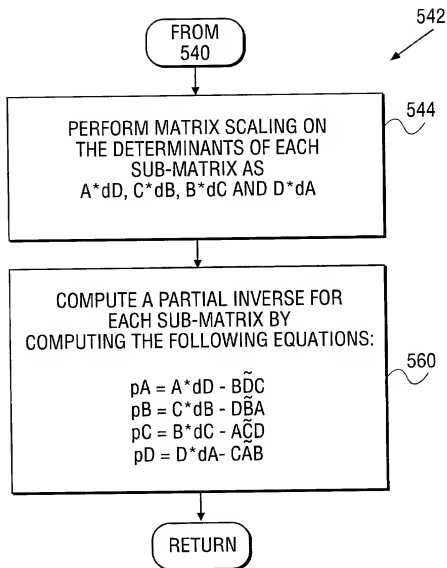


FIG. 13

**FIG. 14**



**FIG. 15**

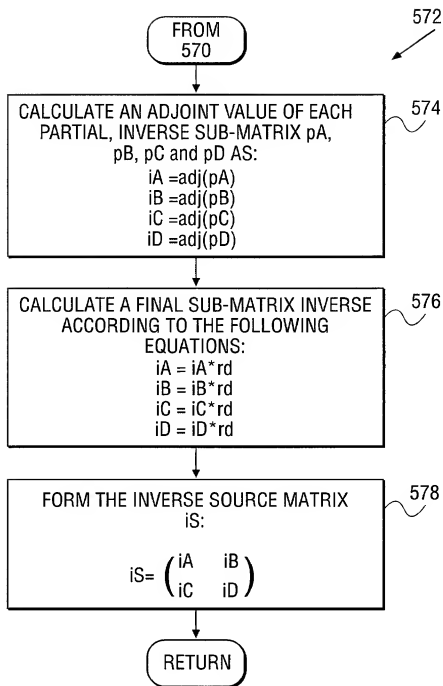


FIG. 16



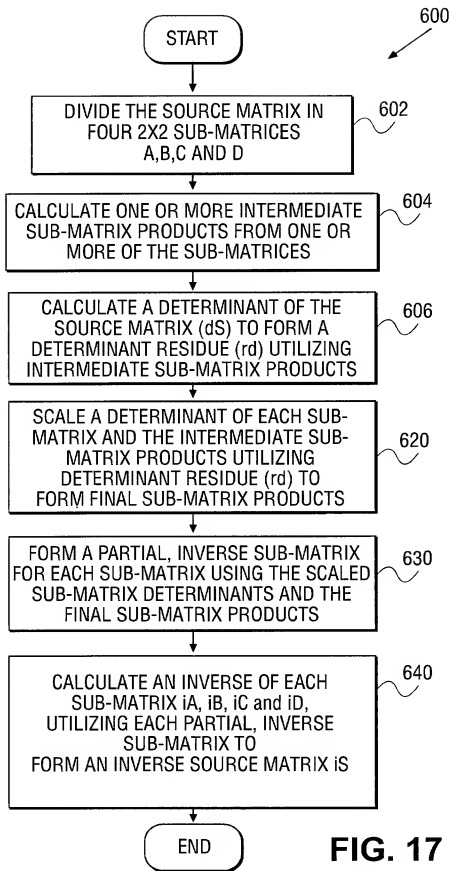


FIG. 17

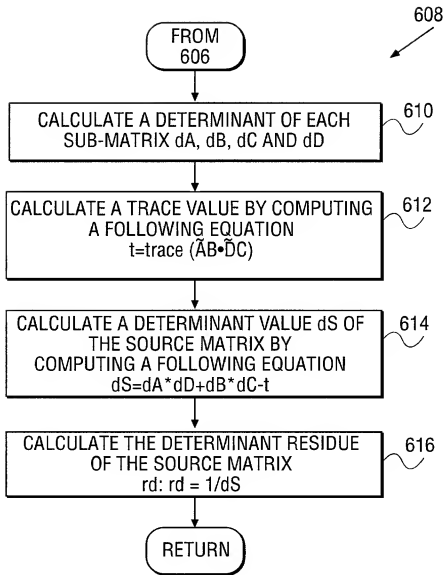


FIG. 18

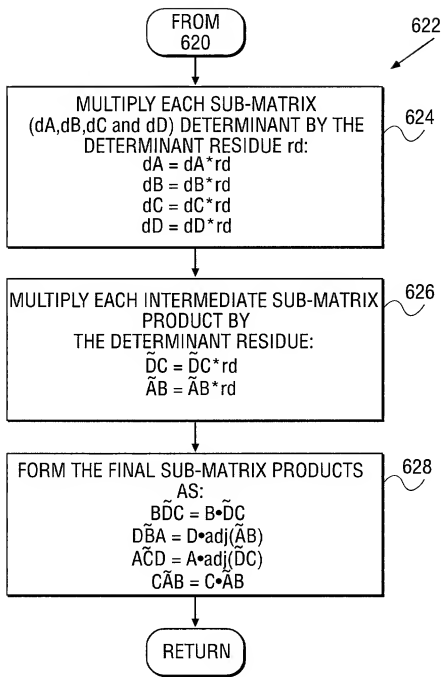


FIG. 19

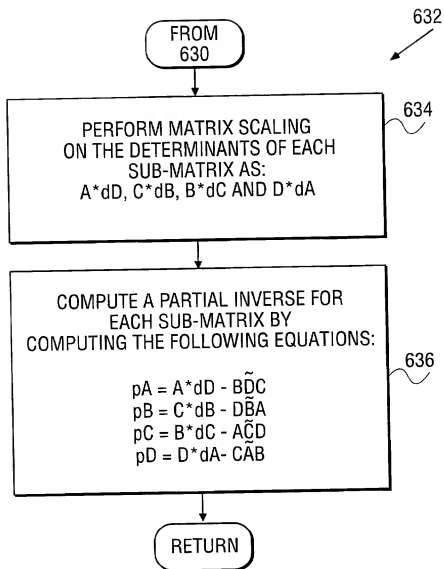
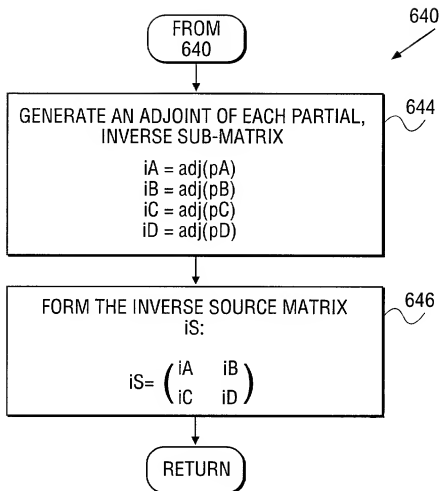


FIG. 20

**FIG. 21**